

Direct Growth of Crystalline Silicon on GaAs by Low Temperature PECVD: Towards Hybrid Tunnel Junctions for III-V/Si Tandem Cells

G. Hamon^{1,2}, J. Decobert³, N. Vaissiere², R. Lachaume⁴, R. Cariou^{2,3}, W. Chen², J. Alvarez⁴, J.P. Kleider⁴ and P. Roca i Cabarrocas²

¹ TOTAL New Energies, 24 cours Michelet, 92069 Paris La Défense Cedex, FRANCE

² LPICM, CNRS, Ecole Polytechnique, Université Paris-Saclay, 91128, Palaiseau, FRANCE

³ III-V lab, 1 avenue Augustin Fresnel 91767 Palaiseau, FRANCE

⁴GeePs; CNRS UMR 8507 ; CentraleSupélec ; Univ Paris-Sud ; Sorbonne Universités-UPMC Univ Paris 06 ; 11 rue Joliot-Curie, Plateau de Moulon, F-91192 Gif-sur-Yvette Cedex, FRANCE

Combining III-V and Si is of high interest to produce tandem solar cells reaching high theoretical conversion efficiencies[1]. With the standard approach, namely the epitaxial growth of III-V on a silicon substrate, crystalline defects caused by the interface polarity issues, the thermal expansion, and lattice mismatches highly prevent from growing a high quality III-V material. We propose here an innovative approach for tandem solar cells, based on the inverse metamorphic growth of crystalline silicon on GaAs[2]. In this approach, we first use MOVPE to grow the AlGaAs top cell on a lattice matched substrate, and then perform low temperature (LT-) PECVD heteroepitaxial growth of Si or SiGe for the bottom cell, as shown in Figure 1. The tandem solar cell is then transferred to a low cost carrier and the GaAs substrate is reclaimed. The main advantages of this technique are that the III-V subcell has a high quality as it is directly grown on a GaAs substrate, and the heteroepitaxy is not subject to the polarity issues encountered in direct growth of III-V on Si. Furthermore, such low temperature c-Si epitaxy prevents from the usual defects induced by the thermal expansion mismatches between the two materials, as it occurs below 200 °C. The LT-PECVD growth technique also allows us to grow highly doped Si on GaAs, which opens up the path to a III-V/Si hybrid tunnel junction. The tunnel junction is a key element to ensure a low resistance and minimum optical losses between subcells. There are many theoretical and experimental studies exploring the tunneling properties of various III-V heterostructures[3][4]. However, the literature about hybrid tunnel junctions (TJ) for III-V/Si multijunction solar cells is very scarce[5][6], despite the huge potential of such devices. Thus, the aim of this work is to study different configurations of the Si/GaAs interface and to find the best architecture for connecting III-V and SiGe subcells in a tandem device, including Si/III-V hybrid tunnel junctions.

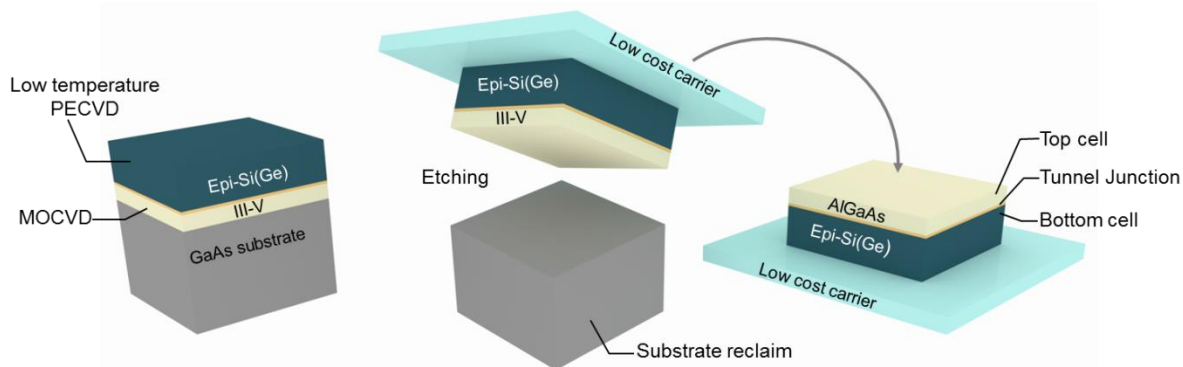


Figure 1 : Principle of the IMPETUS approach: inverted metamorphic growth of Si(Ge) on III-V

- [1] J. P. Connolly, et al. « Designing III–V multijunction solar cells on silicon », *Prog. Photovolt. Res. Appl.*, janv. 2014.
- [2] R. Cariou, et al. « Low temperature plasma enhanced CVD epitaxial growth of silicon on GaAs: a new paradigm for III-V/Si integration », *Sci. Rep.*, vol. 6, p. 25674, mai 2016.
- [3] M. Hermle, et al. « Numerical simulation of tunnel diodes for multi-junction solar cells », *Prog. Photovolt. Res. Appl.*, vol. 16, n° 5, p. 409–418, 2008.
- [4] J. F. Wheeldon, et al. « Performance comparison of AlGaAs, GaAs and InGaP tunnel junctions for concentrated multijunction solar cells », *Prog. Photovolt. Res. Appl.*, vol. 19, n° 4, p. 442–452, juin 2011.
- [5] J. Liang, et al. « Investigation on the interface resistance of Si/GaAs heterojunctions fabricated by surface-activated bonding », *Jpn. J. Appl. Phys.*, vol. 54, n° 3, p. 30211, mars 2015.
- [6] K. Tanabe, et al., « III-V/Si hybrid photonic devices by direct fusion bonding », *Sci. Rep.*, vol. 2, avr. 2012.